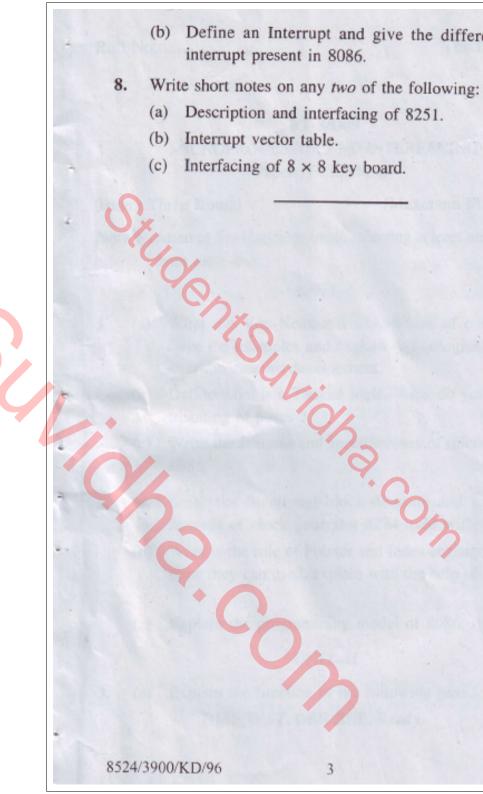
		instruction:	2	Rol	l No.		Total Pag	es:3		
34		(i) MOV [SI], BX.					ха ла уом 8	524		
5		(ii) AND BX, [SI].								
\leq		(iii) AAA.		BT-5/D09						
5		(iv) IMUL CL.				MICROPROCESSOR AND INTERFACING				
		(v) SBB BX, CX.				Paper: ECE-311(E)				
_ ∪ ₩	(a)	Write an 8086 assembly language program to complement the LSB of flag register using LAHF and				hree Hours] Attempt <i>five</i> questions in	[Maximum Marks all, selecting at least one que			
<u></u>		SAHF instructions.				from each unit.	SAHF instructions.			
0	(b)	Explain the different addressing modes of 8086 with				UN	Explain the Miles I-TI			
2		the help of suitable examples.		1.	(a)		ann architecture of comput			
ב ט		UNIT-III				Give the examples an microprocessors deve	d explain technological tren lopment.	nds in		
	(a)	Draw and explain the timing waveforms for read operation of 8086 in minimum mode.			(b)	Define what is a Trist tristating of buses?	tate logic. What do you me	an by		
†	(b)	Compare the features of DRAM and SRAM. 5		(c)	Write the features and specifications of microprocessor					
3	(c)	Explain the concept of Refreshing of DRAM. 5				8086.	g Explain the con O it is	6		
Q.	(a)	Explain the features and functions of DRAM control		2.	(a)		block diagram and explainerator 8284 used with 8086			
<u>5</u>		TMS-4500 with the help of its functional block diagram.		0	(b)	What is the role of Po	inter and Index register in 8	086 ?		
ָ ה	(b)	Explain in detail the features and functions of different				How they can used, ex	xplain with the help of exan	nples.		
<u>.</u>	(0)	types of Memory devices.			(c)	Explain the programm	ning model of 8086.	7		
200		UNIT-IV				UNI	т–п			
7.	(a)	Sketch and explain the interfacing of PPI 8255 to the		3.	(a)	Explain the function of				
3		8086 microprocessor in minimum node.				NMI, TEST, DEN	BHE, Ready.	10		

(b) Describe the operation performed by the following



Define an Interrupt and give the different types of

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interrupt present in 8086.

Interrupt vector table.

Description and interfacing of 8251.

Interfacing of 8 × 8 key board.