

PROCESS SYNCHRONIZATION

- Concurrent access to shared data may result in data inconsistency. Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes.
- Process Synchronization means sharing system resources by processes in such a way that concurrent access to shared data is handled thereby minimizing the chance of inconsistent data. Maintaining data consistency demands mechanisms to ensure synchronized execution of co-operating processes. Process Synchronization was introduced to handle problems that arise while multiple process executions.

ex:- Instructions of co-operating processes can be interleaved arbitrarily. Hence the order of instructions is irrelevant.

Consider two co-operating processes (sharing a variable 'counter') and having the following set of instructions in each of them

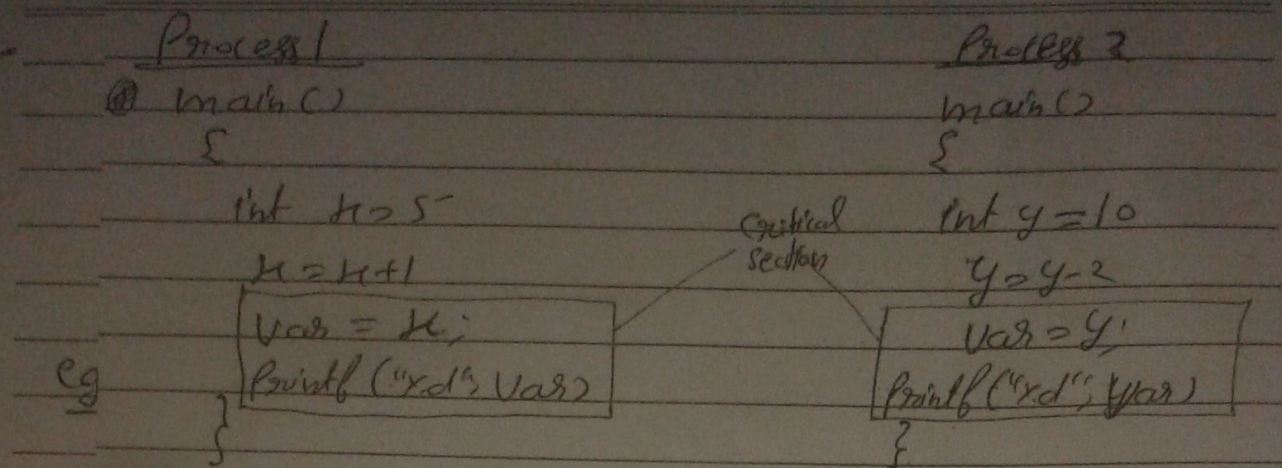
Process 1, (count++)

register1 = counter
register1 = register1 + 1
counter = register1

Process 2 (count--)

register2 = counter
register2 = register2 - 1
counter = register2

where register1 & register2 are local CPU registers



- The concurrent execution of process 1 and process 2 is equivalent to a sequential execution where lower level statements are interleaved in some arbitrary order. One such interleaving is:

T ₀ :	Process 1	main()	
T ₁ :	Process 2	main()	
T ₂ :	Process 1	int x = 5;	
T ₃ :	Process 2	int y = 10;	
T ₄ :	Process 1	x = x + 1;	(x = 6)
T ₅ :	Process 2	y = y - 2;	(y = 8)
T ₆ :	Process 1	var = x;	(var = 6)
T ₇ :	Process 2	var = y;	(var = 8)
T ₈ :	Process 1	printf(var)	o/p = 8
T ₉ :	Process 2	printf(var)	o/p = 8

Notice that we have arrived at the incorrect state "output 8-8" when in fact output should be "output 6-8".

Pizza problem

We would arrive at this incorrect state becoz we allowed both processes to manipulate the variable counter concurrently. A situation like this, where several processes access and manipulate the same data concurrently and the outcome of the execution depends on the particular order in which the ~~process~~ access takes place, is called a race condition.

Race condition $\rightarrow$ if the final output of the execution of processes depends ~~on~~ on the execution of the processes.

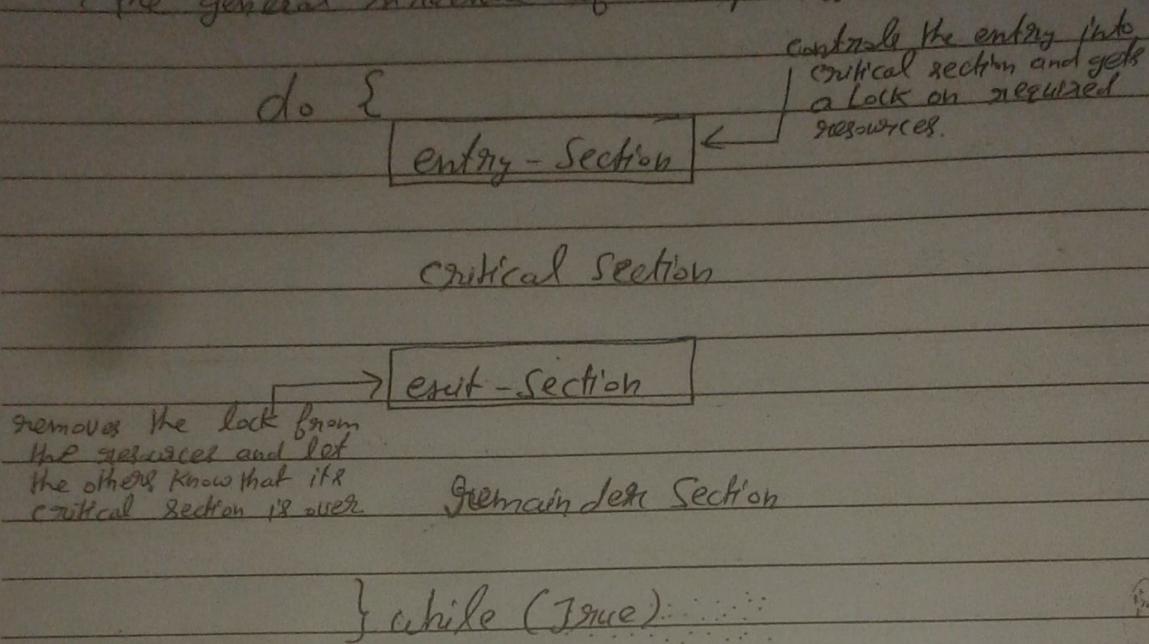
Critical Section Problem

Critical Section $\rightarrow$ Each process has a segment of code, called a critical section, in which the process may be changing common variables (shared variables), updating a Table, writing a file and so on.

When one process is executing in its critical section, no other process is to be allowed to execute in its critical section. That is, no two processes are executing in their critical sections at the same time. The critical section problem is to design a protocol that the processes

Can use to cooperate.

→ the general structure of a process is,



general structure of a process.

→ Each process must request permission to enter its critical section. The section of code implementing this request is the entry section. The critical section may be followed by an exit section. The remaining code is the remainder section.

Progress:- if critical section is empty and some process wants to go to its critical section, then it should be allowed

→ A solution to the critical-section problem must satisfy the following three requirements:

① Mutual Exclusion:- if process P_i is executing in its critical section, then no other processes can be executing in their critical sections.

② Progress:- if no process is executing in its critical section and some processes wish to enter their critical sections, then only those processes that are not executing in their remainder sections can participate in the decision on which will enter its critical section next, and this selection can not be postponed indefinitely

- if no process is in its critical section, and if one or more threads want to execute their critical section then any one of these threads must be allowed to get into its critical section.

(i.e. processes cannot be blocked forever waiting to get into their critical section)

→ waiting should be limited
③ Bounded waiting:-

There exists a bound, or limit, on the number of times that other processes are allowed to enter their critical sections after a process has made a request to enter its critical section and before that request is granted.

• No process should have to wait forever to enter

into critical section

1 (if bounded waiting is not satisfied then it is possible for the starvation).

[B. w is satisfied, when the process requested first will to critical section first]

⇒ There are various solution to the critical section problems as following:-
Solutions

To be		
but for	Software Solutions	Hardware Solutions - O.S. and compiles solutions
	• Dekker's Algo • Peterson's Algo for 2 processes	• Semaphore
	• Bakery Algo for n processes	Here special Instructions supported by Hardware are included in Entry or Exit Section eg:- TestAndSet • Exchange or Swap
		Both for n no. of process • monitor

Petersen's Algorithm $\rightarrow$

$\rightarrow$ It's a software based solution to the critical-section problem.

- Petersen's solution is restricted to two processes that alternate execution between their critical sections and remainder sections. The processes are numbered P_0 and P_1 . when presenting P_i , we use P_j to denote the other process; that is, j equals $1-i$.

$\rightarrow$ Petersen's solution requires two shared data items:-

- `int Turn` - Indicates whose turn it is to enter into the critical section.
if `Turn == i`, then process P_i is allowed to execute in its critical section.

- `boolean flag[i]`;

The flag array is used to indicate if a process is ready to enter its critical section or indicates when a process wants to go into enter into their critical section. For example, if `flag[i]` is `True`, this value indicates that P_i is ready to enter its critical section. Initially false.

$\rightarrow$ In the structure of process in Petersen's algorithm, the entry and exit sections are enclosed in boxes:

- In the entry section, process i first

raises a flag indicating a desire (or ready) to enter the critical section.

- Then Turn is set to j to allow the other process to enter the critical section if process j so desires.

- The while loop is a busy loop, which makes process i wait as long as process j has the Turn and wants to enter the critical.

- Process i lowers the $flag[i]$ in the exit section, allowing process j to continue if it has been waiting.

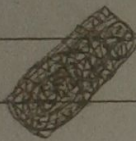
do {

$flag[i] = true;$

$Turn = j;$

while ($flag[j] \&\& Turn == j$);

{do skip}



→ critical section

$flag[i] = false;$

remainder section

} while (True)

The structure of process P_i in Peterson's Solution

→ This solution is correct as having following properties:

1. Mutual exclusion is preserved.
2. The progress requirement is satisfied.
3. The bounded-waiting requirement is met.

To prove property 1, we note that each P_i enters its critical section only if either $\text{flag}[i] = \text{false}$ or $\text{turn} = i$. Also note that, if both processes can be executing in their critical sections at the same time, then $\text{flag}[0] = \text{flag}[1] = \text{true}$. These two observations imply that P_0 and P_1 could not have successfully executed their while statements at about the same time, since the value of turn can be either 0 or 1 but cannot be both. Hence one of the processes - say - P_j - must have successfully executed the while statement, whereas P_i had to execute at least one additional statement (" $\text{turn} = j$ "). However, since, at that time, $\text{flag}[j] = \text{true}$, and $\text{turn} = j$, and this condition will persist as long as P_j is in its critical section, the result of follows; Mutual exclusion is preserved.

To prove properties 2 & 3, we note that a process P_i can be prevented from entering the critical section only if it is stuck in the while loop with the condition $\text{flag}[j] = \text{true}$ and $\text{turn} = j$; this loop is the only one possible. If P_j is not ready to enter the critical section, then $\text{flag}[j] = \text{false}$ and P_i can enter its critical section. If P_j has set $\text{flag}[j]$ to true and is also executing

busy waiting: when the process is waiting in loop (keep repeating in loop).

$Turn == j$. if $Turn == i$, then P_i will enter the critical section. if $Turn == j$, then P_j will enter the critical section. However, once P_j exits its critical section, it will reset $flag[j]$ to false, allowing P_i to enter its critical section. if P_j resets $flag[j]$ to true, it must also set $Turn$ to i . Thus, since P_i doesn't change the value of the ~~value~~ variable $Turn$ while executing the while statement, P_i will enter the critical section (progress) after at most one entry by P_j (bounded waiting).

- Peterson's Algorithm has the following characteristics:-

- A simple algorithm that can be run by two processes to ensure mutual exclusion for one resource (say one variable or data structure)
- Does not require any special hardware
- It uses busy waiting (a spinlock)

Note:- if more than two processes exists in the system, An algorithm for n process process is known as Bakery Algorithm. This algorithm extends the two process Dekker's Algorithm to n process solution for Bakery algorithm.

BAKERY Algorithm

- A better solution for more than one processes
It is based on scheduling algorithm commonly used in ~~business~~ bakeries, ice cream stores and the other locations where order must be made out of chaos.
- On entering the store, each customer receives a number. The customer with the lowest number is served next.
- In this algorithm, every process receives a number before entering its critical section. The process with the lowest number enters the critical section next. In case, two processes P_i and P_j receive the same number and $i < j$, then P_i is served next (i.e. process names are unique and totally ordered). When exiting from its critical section, the process sets its number to 0. This algorithm uses two shared data structures:
 - Choosing 2 array $[0 \dots n-1]$ of Boolean values - Initialized to false
 - Number 2 array $[0 \dots n-1]$ of integer values - initialized to 0.

→ The structure of process is as follows:-

Notation:-

$(\text{Number}[j], j) < (\text{Number}[i], i)$ implies that the above relation is true if $\text{Number}[j] < \text{Number}[i]$ or if $\text{Number}[j] = \text{Number}[i]$ and $j < i$.

```

repeat
  choosing[i] = true;
  number[i] = max(number[0]...number[i-1]) + 1;
  choosing[i] = false;
  for (j=0; j<n; j++)
  { while (choosing[j]);
    while ((number[j] != 0) && (number[j] < (number[i] + 1)));
  }
  < Critical section of Pi >
  number[i] = 0;
  < Remainder section of Pi >
until false;

```

Structure of process P_i used in Bakery Algo:

Mutual Exclusion:- if P_i is in the critical section, it implies that any other process P_k will have a number K' = 0 and number[k] will be greater than number[i] and thus at this time only P_i can execute its critical section. if P_k tries to enter its critical section, it will be looping in the second while statement as its number is greater than number of P_i.

Progress:- It is there becoz a process after exiting from its critical section, makes its number 0 and makes it needs to take a new number (which is obviously going to be larger as compared to the

existing number) to contest with other processes
trying to enter their critical section. Therefore, every
process will be fairly dealt with by being given
an opportunity to enter its critical section
after a finite time (as done in bakery by
assigning Tokens to its customers)

Hardware Solutions

→ Many systems provide special Hardware Instructions that allow us either to Test and modify the content of a word or to swap the contents of two words atomically.

→ The Test-and-set instruction is executed atomically.

```
boolean TestAndSet (boolean *Target)
{
    boolean rv = *Target;
    *Target = True;
    return rv;
}
```

Test-and-set() Instruction.

[- if two TestAndSet() instructions are executed simultaneously (each on diff. PU), they will be executed sequentially]

⇒ In contrast to TestAndSet() instruction, there is Swap() instruction that operates on the contents of two words. It is also executed atomically.

```

void swap(boolean *a, boolean *b)
{
    boolean temp = *a;
    *a = *b;
    *b = temp;
}

```

→ Swap() Instruction

⇒ Test-and-Set() Instruction can be implemented for mutual exclusion by declaring a Boolean variable 'lock', initialized to false.

```

do
{
    while (TestAndSetLock(&lock))
        ; // do nothing

```

// critical section

lock = False;

// remainder section

} while (True);

Mutual exclusion Implementation with TestAndSet()

① TestAndSet (int x)

{ if ($x == 0$)

{ $x = 1$;

return false;

}

return true;

}

int bolt = 0;

{ bolt = 0; process allowed for C.S.
bolt = 1; " not allowed

P_0	P_1	P_2
do		
while (TestAndSet(&bolt))	do	do
{	{ while (TestAndSet(&bolt))	{ while (TestAndSet(&bolt))
do nothing	{ do nothing }	{ do nothing
}	}	}
<C.S.>	<C.S.>	<C.S.>
bolt = 0;	bolt = 0;	bolt = 0;
<G.S.>	<G.S.>	<G.S.>
} while (1);	} while (1);	} while (1);

→ M.S and progress is satisfied here but
B. waiting is not satisfied here becoz after finishing
 P_0 C.S. bolt = 0 and other process are waiting
and now if P_0 wants to go so, it can go in
C.S. becoz bolt = 0.

1
⇒ The Swap() instruction implements mutual exclusion by using global Boolean variable lock is declared and is initialized to false. In addition, each process has a local Boolean variable key, the structure

```
do {  
    key = True;  
    while (key == True)  
        swap(&lock, &key)  
        // critical section  
    lock = false;  
        // remainder section  
} while (True);
```

Mutual-exclusion implementation with Swap.

⇒ These algorithms satisfy the mutual-exclusion requirement. They do not satisfy the bounded-waiting requirement.

①

② Swap or Exchange

Swap (int ^{bolt}x, int y)

{ int Temp;

Temp = x;

x = y;

y = Temp;

return y;

}

here int bolt = 0;

K=1;

K=1

K=1

P₀

P₁

P₂

do {

int K=1;

{ while (Swap (bolt, K))

{ do nothing }

<C.S.>

bolt = 0;

<P.S.>

} while (1);

do

int K=1;

{

while (Swap (bolt, K))

{ do nothing }

<C.S.>

bolt = 0;

<P.S.>

} while (1);

do

int K=1;

{ while (Swap (bolt, K))

{ do nothing }

<C.S.>

bolt = 0;

<P.S.>

} while (1);

//

Semaphores

Semaphore $\rightarrow$ Semaphore is a global shared variable that, apart from initialization, is accessed only through two standard atomic operations: $\text{wait}()$ and $\text{signal}()$

(i) $\text{wait}(\text{Semaphore } S)$ written as $P(S)$

(ii) $\text{Signal}(\text{Semaphore } S)$ written as $V(S)$

Classical definition of wait and signal:-

wait()

```
int s=1
wait(s)
{
    while (s <= 0)
    {
        do nothing
    }
    s--;
}
```

signal()

```
signal(s)
{
    s++;
}
```

Note:- wait is Entry section
 signal is Exit section

→ Mutual exclusion implementation with semaphores is as:-

```
do {
    waiting (mutex);

    <critical-section>

    signal (mutex);

    <remainder-section>

} while (true);
```

Mutual exclusion implementation with semaphores-

→ It satisfies mutual exclusion and progress but bounded-waiting is not satisfied.

- Busy-waiting → while a process is in its critical section, any other process that tries to enter its critical section must loop continuously in the entry code. Busy waiting wastes CPU cycles that some other process might be able to use productively. This type of semaphore is also called spinlock.

- ~~Sem~~ Spinlock are useful in multi-processor systems. Advantage of a spinlock is that no context switch is required when a process

(4)

must wait on a lock and content may take considerable time.

- To overcome the need for busy waiting, we can modify the definition of the wait and signal semaphore operations. When a process executes the wait operation and finds that the semaphore value is not positive, it must wait. However, rather than busy waiting, the process can block itself. The block operation places a process into a waiting queue associated with the semaphore and the state of the process is switched to the waiting state. Then, control is transferred to the CPU scheduler which selects another process to execute.

A process that is blocked, waiting on a semaphore S , should be restarted when some other process executes a signal operation. The process is restarted by a wakeup operation which changes the process from the waiting state to the ready state. The process is then placed in the ready queue.

- Spin lock solution is structure implementation

(21)

→ They are of two types of semaphores

- (1) Counting - Semaphore
- (2) Binary - Semaphore

① Counting Semaphore is

here, we define a semaphore as 's' struct

```

typedef struct {
    int value;
    struct t_list * list;
} semaphore;
  
```

Each semaphore has an integer value and a list of processes. List: When a process must wait on a semaphore, it is added to the list of processes. A signal() operation removes one process from the list of waiting processes and awakens that process.

- The semaphore operation can be defined as:-

wait(s)

```

wait (Semaphore s)
  
```

```

s.value--;
  
```

```

if (s.value < 0)
  
```

```

{ Block the process and
  
```

```

add this process to
  
```

```

the list of semaphores
  
```

```

}
}
  
```

```

and insert in ready
  
```

```

block queue of semaphore
  
```

```

{ pick one process from
  
```

```

if (s.value <= 0)
  
```

```

s.value++;
  
```

```

signal (Semaphore s)
  
```

```

signal()
  
```

-if the semaphore value is negative, its magnitude is the number of processes waiting on that semaphore

P_0	P_1	P_2
do	do	do
{ wait(S)	{ wait(S)	{ wait(S)
<C.S>	<C.S>	<C.S>
Signal(S)	Signal(S)	Signal(S)
<R.S>	<R.S>	<R.S>
} while(1)	} while(1)	} while(1)

-here is bounded waiting

FIFO queue only satisfy the bounded waiting

$\Rightarrow$ if S value is initially 0, then deadlock will be there & all processes are blocked

S value initially $[1] \rightarrow [0] \rightarrow [-1] \rightarrow [-2]$

$\Rightarrow$ Deadlock & starvation

The implementation of a semaphore with a waiting queue may result in a situation where two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes, when such a state is reached, these processes are said to be deadlocked

eg: Set S and Q to value 1:

P_0	P_1
wait(S);	wait(Q);
wait(Q);	wait(S);
}	}
Signal(S);	Signal(Q);
Signal(Q);	Signal(S);

Suppose that P_0 executes wait(S) and P_1 executes wait(Q), when P_0 executes wait(Q), it must wait until P_1 executes signal(Q). Similarly when P_1 executes wait(S)

it must wait until P_0 executes `signal()`

Since these signal operations cannot be executed P_0 ?

P_1 deadlocked

• Starvation:- a situation where processes wait indefinitely within the semaphore

- Indefinite blocking may occur, if we add and remove processes from the list associated with semaphore in LIFO order.

② Binary Semaphore:-

→ A Binary Semaphore is a semaphore with an integer value that can range only between 0 and 1.

→ struct BinarySemaphore
{

Boolean Value;

struct process *L;

} BS;

→ The `wait()` & `signal()` operation can be redefined as follows:-

wait (Binary Semaphore BS)	Signal (Binary Semaphore BS)
{	{
if (BS.value == 1)	if (Blocked queue of Semaphore not empty)
{	{
BS.value = 0;	
}	pick one process from blocked queue and insert in ready queue
else	}
{	else
Block the process &	{
Insert the process in blocked queue of Semaphore	BS.value = 1;
}	}
}	}

eg:-		
P ₀	P ₁	P ₂
do	do	do
{ wait(S)	{ wait(S)	{ wait(S)
<C.S>	<C.S>	<C.S>
Signal(S)	Signal(S)	Signal(S)
<R.S>	<R.S>	<R.S>
{ while(1);	{ while(1);	{ while(1);

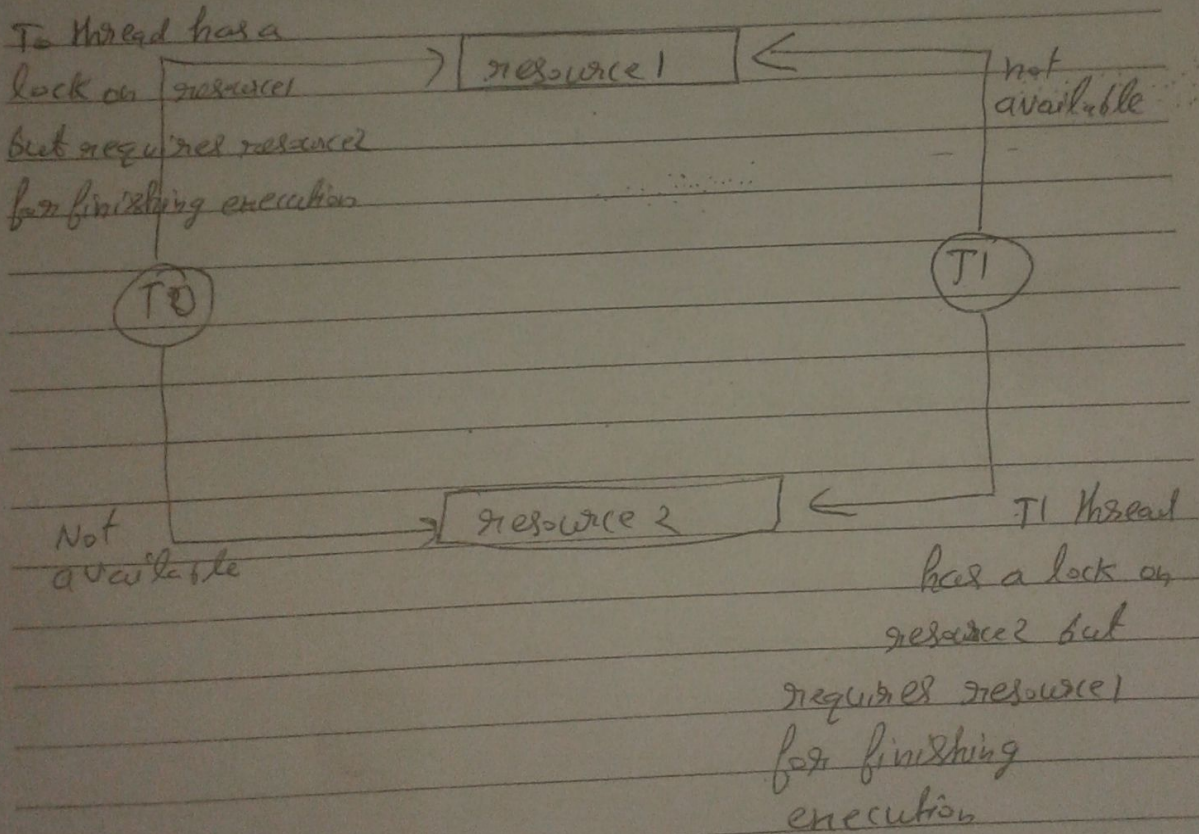
Initially B.S value = 1

0 --- firstly 1 to P₀ then process should be allowed to go to C.S.

Deadlock

Deadlock $\rightarrow$ A set of processes is in a deadlock state when every process in the set is waiting for an event that can be caused only by another process(es) in the set.

or
Deadlock is the state of the system where the two or more processes are waiting on some event to happen, which never happens, then these processes are said to be involved in the deadlock.



Deadlock characterization :-

⇒ Necessary Conditions for deadlock :-

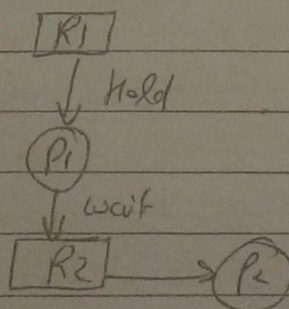
→ A deadlock situation can arise if the following four conditions hold simultaneously in a system:-

1. Mutual Exclusion:-

Resources cannot be shared. only one process at a time can use the resource. if another process requests that resource, the requesting process must be delayed until the resource has been released.

2. Hold and wait:-

A process must be holding at least one resource and waiting to acquire additional resources that are currently being held by other processes.

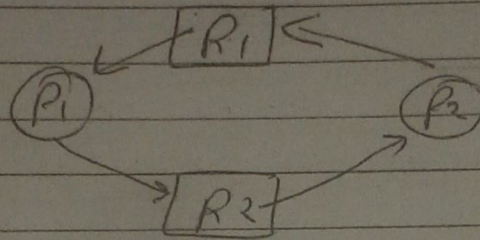


3. No Preemption:-

Resources cannot be preempted; that is, a resource can be released only voluntarily by the process holding it, after that process has completed its task.

4. Circular wait :-

A set $\{P_0, P_1, P_2, \dots, P_n\}$ of waiting processes must exist such that P_0 is waiting for a resource held by P_1 , P_1 is waiting for a resource held by P_2 , ..., P_{n-1} is waiting for a resource held by P_n and P_n is waiting for a resource held by P_0 .



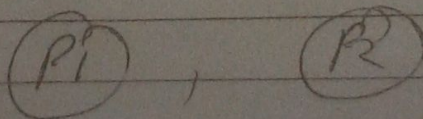
→ if all the above four conditions are existing simultaneously in the system, then definitely there exist a deadlock

- all the above four conditions are not truly independent because the circular wait condition implies the hold-wait condition

⇒ Resource - Allocation Graph :-

- A Tool for recognizing deadlock is "Resource - Allocation Graph" (RAG).

- Pictorially, we represent each process P_i as a circle



- each Resource Type R_i is represented as a Rectangle

$[R_1]$

$[R_2]$

Since Resource-Type R_i may have more than one instance (like files, I/O devices (printers & DVD) or more than one CPU), we represent each such instance as a dot within the rectangle

$[R_1 \cdot]$

- Single instance of resource R_1

$[R_2 \cdot \cdot]$

- two instances of resource R_2

$$R.A.G. = G(V, E)$$

vertices include

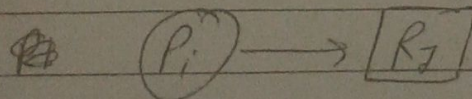
processes, resources

Edges include

Allocation, Requesting

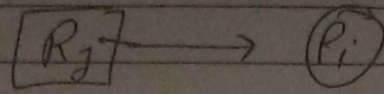
Its edges are of Two Types:-

- Request edge :- A directed edge from process P_i to resource type R_j is denoted by $P_i \rightarrow R_j$; It signifies that process P_i has requested an instance of resource type R_j and is currently waiting for that resource

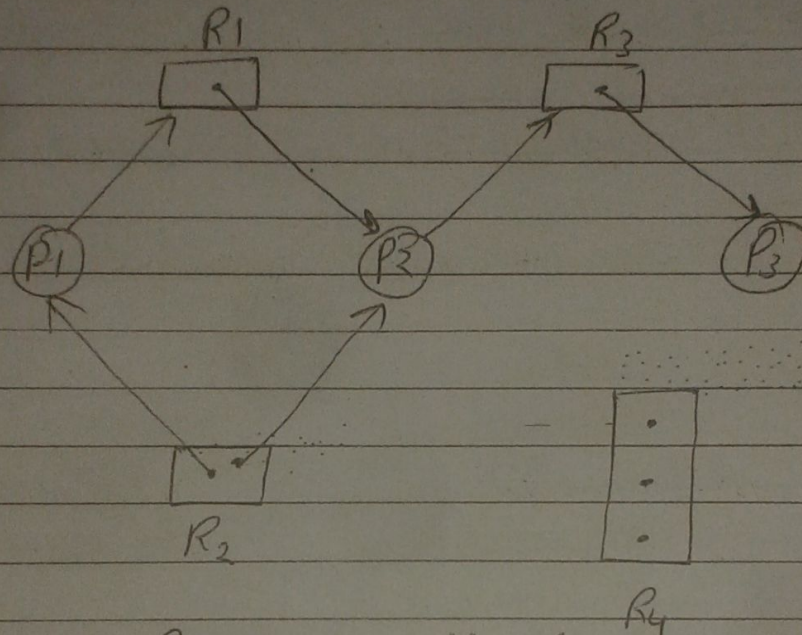


- Assignment edge :- A directed edge from resource R_j to process P_i is denoted by $R_j \rightarrow P_i$; it signifies that

an instance of resource type R_i has been allocated to process P_i .



eg:-



Resource-allocation graph

process states:-

- Process P_1 is holding an instance of resource type R_2 and is waiting for an instance of resource type R_1 .
- Process P_2 is holding an instance of R_1 and an instance of R_2 and is waiting for an instance of R_3 .
- Process P_3 is holding an instance of R_3 .