

Roll No. ....

**24488**

**B. Tech. 7th Semester (CSE)  
Examination – June, 2016**

**COMPILER DESIGN**

**Paper : CSE-405-F**

***Time : Three Hours ]***

***[ Maximum Marks : 100***

*Before answering the questions, candidates should ensure that they have been supplied the correct and complete question paper. No complaint in this regard, will be entertained after examination.*

***Note :*** The candidate have to attempt *first compulsory* question and *one* question from each Section. All questions carry equal marks.

**1. Explain the following :**

**20**

- (i) Translator
- (ii) Passing
- (iii) SLR
- (iv) Error

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**P. T. O.**

### SECTION - A

2. What is Compiler ? Explain the structure of Compiler in detail. 20
3. Describe the following : 20
- (i) Conversion from Regular expression to Finite Automata.
  - (ii) Implementation of Lexical Analyzer.

### SECTION - B

4. Explain the following in detail : 20
- (i) Context free Grammar
  - (ii) Role of Parser
5. Define Parsing. Explain in detail about Parsing Technique. 20

### SECTION - C

6. What is Syntax directed Translation Scheme ? Also explain the implementation of Syntax directed translation. 20

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7. Describe the following : 20
- (i) Canonical LR Parser
  - (ii) LALR

### SECTION - D

8. What is symbol Table ? Explain in detail about its contents and data structure. 20
9. (i) What do you mean by Code Generation ? Explain. 10
- (ii) Explain the register allocation for temporary and user defined variables. 10

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