END TERM EXAMINATION

FOURTH SEMESTER [B.TECH] MAY-JUNE 2016

(NEW) Paper Code: ETEC-204 Subject: Analog Electronics-II Time: 3 Hours Maximum Marks: 75 Note: Attempt any five questions including Q.no. 1 which is compulsory. Select one question from each unit. (a) Design an OPAMP as a buffer amplifier and voltage multiplier. (5x5=25) (b) Explain circuit diagram and working of Colpitt's oscillator. Also mention the frequency of oscillations. (c) Calculate the period of oscillations of Hertley oscillator having L1 = 1 mH, L2 = 2 mH and $C = 0.1 \mu F$. (d) Discuss Class C and AB amplifiers. (e) Explain second order low pass filter with a suitable diagram. Q2(a) Discuss op-amp as an integrator giving its applications. A 5 mV, 1 kHz sinusoidal signal is applied to the input of an op-amp integrator for which $R = 100 \text{ K}\Omega$, and $C = 1 \mu\text{F}$. Find the output voltage. (b) The input to the differential OPAMP is a sinusoidal voltage of peak value 5 mV and frequency 2 kHz. Find the output if $R = 100 \text{ k}\Omega$ and $C = 1 \mu F.(6)$ (a) A differential amplifier has an open circuit gain of 100. The input signals are 3.25 V and 3.15 V. Determine the output voltage. What conclusion do you draw from this calculation? (4.5)(b) Explain the following: (i) Active Loads (ii) Virtual Ground (iii) Current Mirrors (iv) Voltage to Current converters Unit-II (a) Draw the circuit of crystal oscillator and explain its operation. (b) Explain the working of an op-amp square wave generator. Derive an expression for the frequency of oscillation. What is the maximum amplitude of the square wave and how can it be varied? (6.5)(a) Discuss principle and working of Sawtooth Waveforms Generators. Q5 (b) Calculate the frequency of oscillations of a Colpitts oscillator with $C_1 = C_2 = 400 \text{ pF}$ and L = 2 mH. (6.5)(a) Discuss power dissipations in transistors. (6) (b) Explain Clipping and Clamping circuit with suitable examples. (a) Explain the working of a complementary symmetry class-B push pull amplifier. Show that its maximum conversion efficiency is 78.5%. What will be the conversion efficiency when power dissipation is maximum. (6) (b) Discuss the working of a log-amplifier with a suitable diagram, which provides saturation current and temperature compensation. Unit-IV (a) Discuss wide band pass filter along with its frequency response curve. Also, discuss its frequency. (6) (b) Write a short note on HPFs and LPFs. (6.5)(a) Discuss Butter worth and Chebyshev filters. Q9 (6.5)(b) Discuss IC voltage regulators. (6)